

Notice of References Cited	Application/Control No. 10/090,239	Applicant(s)/Patent Under Reexamination GHIA ET AL.	
	Examiner Anh Q. Tran	Art Unit 2819	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
AI	U	Pub. No. : US 2001/0033188 A1, Aung et al, Clock Data Recovery Circuitry Associated with PLD circuitry, 10/25/01.
AI	V	Pub. No. : US 2003/0039168 A1, Chan et al, Serializer/Deserializer Embedded in a PLD, 2/27/03.
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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